

## 1.0 DOEACC Centre, Aurangabad - An Introduction

DOEACC Centre, Aurangabad (the institute) is an unit of DOEACC Society, which is an Autonomous Scientific Society of Department of information Technology, Ministry of Communications and Information Technology, Govt. of India. The DOEACC Centre (formerly Centre for Electronics Design and Technology of India, (CEDTI)), Aurangabad was established in the year 1986 jointly by Department of Information Technology, Ministry of Communications & Information Technology (erstwhile the Department of Electronics), Govt. of India, Govt. of Maharashtra and Dr. B.A.M. University, Aurangabad (Maharashtra). It has been established primarily to train man-power in electronics design and technology. The Centre is engaged in the area of manpower training, product design & development, applied research and consultancy in the field of Information, Electronics, Communications Technology (IECT). Its one of the objectives is to disseminate knowledge through ICT (e-learning).

### 1.1 Location

Aurangabad is located in Central Maharashtra and has always been a prominent region on the Deccan Plateau. Having been inhabited since the stone age, it has a long artistic and cultural history with various monuments like Bibi-ka-Makbara (replica of Taj Mahal), Panchakki, Daulatabad Fort (Second Capital of the Delhi Sultanate), Tomb of Mughal Emperor “Aurangajeb” and Aurangabad Caves, etc.. Apart from these, the city is surrounded with world heritage sites like Ajanta (101 km) and Ellora (27 km), holy places like Paithan (60km) and Shirdi (120km). The industrial area of city has developed rapidly and was the fastest growing industrial city. Aurangabad, about 388 km north-east of Mumbai, has Airport and Railway Station and is connected to Mumbai and New Delhi by Air and Rail. Pune is about 230 km and is well connected by road to all major cities.

The institute, having a beautiful building of 11,500 square meters on 18.75 acres of hill side land, is located within the campus of Dr. B.A.M University at a distance of 05 km from the Central Bus Stand and 8 km from the Railway Station.

### 1.2 Institute’s Thrust Areas

- Instrumentation/Automation for various sectors with focus on Agriculture Sector or Agri-Electronics.

### 1.3 Other Areas

- e- learning
- Information Security
- Information Technology
- Opto-electronics
- Power Electronics
- Linux & Networking
- Simulation/VLSI Design
- Embedded System Design
- Agri-electronics
- Multimedia & Animation
- PCB Design, Fabrication & SMD
- Industrial Design & Prototype Development

## **1.4 Activities**

### **1.4.1 Academic / Training Programs**

A) Long Term Courses.

A.1 Doctoral (Ph.D) Program in Engineering & Technology.

A.2 AICTE approved Long Term Courses.

- Four semesters (2 years) M Tech in Electronics Design and Technology
- Six semesters(3 Years) Diploma in Electronics Production and Maintenance (DEPM)

#### ***Salient Features***

- Selection of candidates through national level aptitude test and interview
- Autonomous Courses under Dr. B.A.M. University, Aurangabad.
- Practical oriented quality education with emphasis on design and project work.
- 100% employment and self-employment opportunities

### **B) Short Term Courses**

The institute is known for its informal Short Term industry oriented Courses (STCs) i.e. computer oriented, design oriented, specialized, manufacturing technology oriented courses of duration 01-52 weeks to update the knowledge of industry professionals and academicians with latest technology trends. The Centre is offering Short Term Courses in the areas of IECT such as Embedded System Design, VLSI Design, Information Security, Information Technology, e-learning, Multimedia and Animation, Industrial Design, CAD/CAM, Opto. Electronics, Power Electronics, PCB Design and Fabrication etc.. Apart from above, the Centre has also established laboratory in the area of Agri-Instrumentation & Control Microprocessor and Micro controller, Analog & Digital Techniques, Consumer Electronics, Test and Measurements, Mechanical Workshop and in allied fields.

#### ***Salient Features***

- Courses in emerging areas of Electronics, IT and allied fields
- Application Oriented
- Part Time and concurrent with on-going studies.
- Targeted towards Industry Professionals and Academicians
- Custom designed & Modular Courses
- Corporate Training

### **C) Schemes**

The Schemes launched in line with the national policy are as under:-

- Hardware Technical Consultancy Scheme (HTCS) in 1995.
- Software Technical Consultancy Scheme (STCS) in 1996.
- CEDTI Franchise Scheme (CFS) in 1999.
- Certificate Course in Information Technology (CIT) in 1999.
- Bioinformatics Courses in 2003.
- DOEACC Scheme on Hardware Courses jointly with MAIT in 2005.
- Course in Computer Concept (CCC) in the region in 2007.

- ITeS-BPO (Banking) Course jointly with Indian Institute of Banking & Finance (IIBF), Mumbai in 2009 (being launched).

### **Salient Features**

- Value addition
- Enhanced employment opportunity.
- Opportunity to learn new hardware & software packages concurrently with on going studies/work.
- Centralized Examination & Certification.
- Quality education to masses at affordable cost.

### **1.4.2 Industry Interaction**

The institute is also providing the services like product design & development, product engineering, proto-type development, process automation, consultancy services, services and maintenance to industries. The institute is also making all efforts to create best infrastructure to provide quality services to industry in servicing and maintenance of sophisticated instruments/ machines, support in technology absorption and procurement of latest equipment/ machines.

### **1.4.3 R & D, Projects and Consultancy**

Post Graduate level academic projects are of one (01) year duration, whereas Diploma level projects are of one (01) semester (six months) duration. Students are encouraged to interact with industry to expose them to industry environment and motivated to undertake real problems of industry as their innovative project work, guided by the faculties. In addition to above, the institute also undertakes Government as well as industry sponsored projects. Some of them are “Training of Teachers in e-learning” , “Information Security Education & Awareness” and Women Empowerment through Value Added Skill Development in IECT”. Apart from above, the consultancy is also provided to the industry.

### **1.5 Laboratories**

Beautifully landscaped building with 11,500 Sq. meters built-up area, situated on 18.75 acres of hillside land in the university campus, is housing lecture halls and modern facilities along with the following Laboratories:

➤ Embedded System Design	➤ Agri Instrumentation and Control
➤ Simulation/ VLSI Design	➤ Opto electronics
➤ Information Security	➤ Power Electronics
➤ E-learning	➤ PCB Design and Fabrication
➤ Multimedia & Animation	➤ SMD Facility for Low Volume Production
➤ Software Development	➤ Prototype Development
➤ *Linux and Networking	➤ Mechanical Workshop with Fitting Shop
➤ Industrial Design	➤ Consumer Electronics
➤ CAD/CAM	➤ Test and Measurement
➤ Computers Centre	➤ Analog & Digital
➤ Service & Maintenance	➤ *Language (English) with appropriate Software

\*Under development

## 1.6 Other Amenities / Facilities:

➤ Lecture Halls	➤ Placement Cell
➤ Seminar Hall	➤ Cafeteria
➤ Conference Hall	➤ Boy's Hostel
➤ Auditorium	➤ PG Boy's Hostel
➤ Local Area Network with 225 (100 Mbps ) Nodes.	➤ Working Professional's Hostel
➤ Leased line internet Connectivity	➤ Guest House
➤ Comprehensive Library with DELNET Facility & e-Journal accesses (Area -331 sq mtr, Books – 7649, Bound volumes – 476, Journals – 10 No, Online Access – 570 IEEE Journals & 1700 ISO Standards, Learning Resources (LRs) –59 in 39 CDs / DVDs	➤ Vehicle Parking
➤ Video conferencing & information (touch screen) kiosk	➤ Stores (172 Sqm)
➤ Uninterrupted Power supply (63 KVA DG Set Back-up)	➤ Recordroom (143 Sqm)

## 1.7 Achievements

- Trained about 900 students through AICTE approved courses i.e. Diploma in Electronics Production & Maintenance (DEPM) and Master of Technology (M. Tech).
- Examined & Certified about 2,25,000 candidates through various Franchisee/Accredited Schemes & Short Term Courses.
- Conducted examinations of “Course on Computer Concept (CCC)” of about 1,70,000 candidates in the region between Oct, 07 to Jun,09.
- Trained about 600 teachers from schools/colleges/university in the area of “E-learning”.
- Excellent response to AICTE approved DEPM, M. Tech (EDT) Courses and Ph.D. Program.
- Almost 100% placement/employment of the passed out students.
- Employment to the students in leading and reputed organizations like C.G Coral. Lucent India, Texas, L&T, HCL, Wipro Technologies, BITS, IIT, CEDTI, BEL, HAL, ISRO, DRDO, BARC, ECIL, Messung, Thermax, Honeywell Cyrus logic L&T EMSYS.
- Launched ‘O’ and ‘A’ level Courses under DOEACC Scheme on Hardware Courses in 2005. 113 Training Institutes were accredited for providing hardware training all over the country upto 31<sup>st</sup> Mar, 09. About 7200 candidates registered and approx. 18000 module candidates appeared in five (5) national level examinations upto 31<sup>st</sup> Mar, 2009.
- Launched Ph.D. Program in Engineering and Technology with effect from Academic Year 2006-07.
- Design and Developed Electronics Products and Software for industry.
- Completed / in progress the projects funded by Government of India.
- Provided consultancy and other services to Industry like Bajaj Auto Ltd, Videocon, Sterlite, Siemens, Meltron, Maharashtra Police Wireless, etc.
- Effective utilization of Infrastructure & Manpower to become self sufficient.
- Organized national level seminar / workshop in Agri-Electronics, Electronics Product Design, Neural Networks, e-learning, etc.

## 1.8 New Initiatives

- To launch ITeS-BPO (Banking) Scheme jointly with IIBF, Mumbai for XIIth qualified youths, particularly from rural India for enhancing their employment opportunities in the year 2009.
- To launch further more Short Term Courses and Schemes in emerging areas of IECT such as in the areas of VLSI Design in e-mode (online) in collaboration with IIT Bombay, Embedded System Design, e-learning, Information Security, CAD/CAM and Industrial Design, etc.
- To launch Post Graduate Engineering Programmes in M.Tech in Electronics with specialization in VLSI Design &/or Embedded System Design and M.Tech in Computer Science with specialization in Information Security.
- To prepare and submit project proposals in the area of e-learning, Agri-electronics, Opto-electronics, VLSI and Embedded System Design.
- To become self-sufficient in energy and water resources utilizing the renewable and natural resources.

## 2.0 Programs (Long Term Courses)

### *The Institute Offers:*

- a. AICTE approved six semesters (three years) Diploma in Electronics Production and Maintenance (DEPM) to meet the requirement of industry by creating dedicated and immediate productive technicians.
- b. AICTE approved four semesters (two years) M. Tech in Electronics Design and Technology [M. Tech (EDT)] to produce Electronics Design Engineers.

These courses are practical oriented and are designed with an emphasis on design and project work. The quality of education is maintained by periodic review and update of syllabus considering the latest trends and needs of industry, in-depth study by the students through semester system, transparent evaluation system and flexibility being autonomy granted to the Centre by Dr. B.A.M. University, Aurangabad (M.S). The Centre enjoys the reputation of its students getting employed in reputed industries and organizations almost immediately on their completion of courses or settled as successful entrepreneur.

- c. Doctoral (Ph.D.) Program in Engineering & Technology.

The Ph.D. Program was launched in July, 2006 to provide quality Human Resource for teaching and research by utilizing facility and expertise available at the institute and its linkage with premier academic institutions and industry. Efforts are being made through this program to inculcate the appropriate attitude, aptitude, knowledge and skills required for the above professions. The quality of education is maintained by rules and regulations for admission, selection of guide, periodic (semesterwise) submission of progress and its review by appropriate expert committee, assignment/evaluation of synopsis & thesis by external subject experts before submission to university, guidelines for research scholars and guides, etc.

## **2.1. DIPLOMA IN ELECTRONICS PRODUCTION AND MAINTENANCE (DEPM)**

This is a three years (Six Semesters) course which grooms students for a career as Production/Maintenance Supervisor or Design Assistant in Electronics or allied industry or entrepreneur. The course is approved by AICTE, New Delhi and Board of Technical Education (BTE), Maharashtra (India).

### **2.1.1 Eligibility**

X<sup>th</sup> standard passed from a recognized Board with a minimum average score of 50% (relaxable to 45% for candidates belonging to SC/ST category only) in Mathematics and Science subjects. Those who have appeared for the X<sup>th</sup> standard examination and awaiting results before June 30, 2009 are also eligible to apply.

### **2.1.2 Age Limit**

The candidate's age should not be less than 15 years and more than 19 years as on July 01, 2009. The relaxation in upper age limit is two (2) years for SC & ST candidates and five (5) years for person with Disabilities (PD), candidates.

**2.1.3 Intake** 72 (30 + 16 + 09 + 05 + 12)

**2.1.4 Category** Open/OBC/SC/ST, & PD / Foreign National  
Seats are reserved as per Govt. of India Rules, AICTE and/or University Approval.

### **2.1.5 Selection**

#### **(A) National Applicants**

Candidates will be short listed based on the minimum eligibility criteria. The selection for admission to DEPM program from the short listed candidates will be based on their performance in a written entrance test and interview, to be conducted in the month of July/August. Seats are reserved for candidates belonging to reserved categories as per Government of India Rules and AICTE, New Delhi Approval.

Provisional admission will be granted on the basis of order of merit of the candidate as per the category-wise merit list, prepared on the basis of performance of candidates in written test and interview. As per admission norms, the admission process continues till approved intake of seventy two (72) candidates are admitted to the DEPM Course by operating the category-wise main (selected) list and then, the category-wise waiting list, if required or a time limit decided by the competent authority is over, which ever is earlier. The category-wise main (selected) list and waiting list of the candidates for admission to 23<sup>rd</sup> batch (2009-10) of DEPM Course will be displayed on the Notice Board of this Institute. The date and time for operation of the waiting list shall also be declared alongwith the list of selected candidates. All the waitlisted candidates should make themselves available at the time of operation of the waiting list, otherwise their claim shall be forfeited. The waitlisted candidates, available at the time of operation of the waiting list, shall be provisionally admitted as per the merit of the category-wise waiting list. The selected main and waiting list candidates are required to register on the day as notified alongwith the list displayed by making payment as mentioned in section 2.1.13, otherwise their claim shall be forfeited. Admission process of the DEPM Course is completed when the approval intake of

seventy two (72) candidates are provisionally admitted and registered or a time limit decided by the competent authority is over, whichever is earlier.

**Admission in the event of cancellation of secured admission by a student is as follows:**

If any vacancy arises due to leaving the course by a registered student after completion of admission process, the vacancy may be filled on case to case basis at the discretion of the Director as the admission process is completed, by following below mentioned procedure:

- i) The students from the selected 60 candidates, who could not reach the Centre for admission on the preintimated day because of legal and/or genuine reason(s) and approaching / contacting the institute are first considered for filling the said vacancy.
- ii) Preference shall be given as per the ranking in common merit list.
- iii) After (i) & (ii), the candidates, who have not been offered the admission and approaching, shall be considered.
- iv) Then, the candidates, as per order of merit shall be informed about the offering of the admission.

**B) International Applicants (Foreign Nationals)**

The seats are reserved for foreign nationals as per Government of India Rules and University approval. Admission of Foreign Nationals is subject to guidelines, laid down by Government of India from time to time. Foreign nationals may apply for admission to DEPM course subject to fulfilling the minimum eligibility requirements through proper channel. Their application will, however, be considered separately on first cum first serve basis as per the procedure, mentioned in **ANNEXURE-I**.

Foreign nationals are required to obtain International Student Bulletin containing the application form for eligibility cum admission (**Annexure-I(A)**) and declaration & undertaking format (**Annexure-I(B)**), on payment of Rs.2500/- or equivalent foreign currency. Further, they are required to submit the duly filled eligibility cum admission form alongwith demand draft of the prescribed eligibility fee and the required supporting documents to DOEACC Centre, Aurangabad, Maharashtra (India).

**2.1.6 Academic Calendar – Refer ANNEXURE – II**

**2.1.7 Scheme of Instruction**

Every student has to register for all the subjects of a Semester as mentioned below.

Sr. No	Code No	Subject	Credit Point Theory + Practical
<b>Semester I</b>			
1.	D101	English	2
2.	D102	Mathematics I	3

3.	D103	Physics I	3+1
4.	D104	Chemistry I	3
5.	D105	Electrical Technology I	3
6.	D106	Workshop Technology I	2+4
7.	D1P1	Drawing (Mechanical ) I	0+3

### **Semester II**

1.	D201	Mathematics II	3
2.	D202	Physics II	3
3.	D203	Chemistry II	3
4.	D204	Electrical Technology II	2+1
5.	D205	Workshop Technology II	2+3
6.	D206	Analog & Digital Electronics I	3+1
7.	D2P1	Drawing ( Mechanical ) II	0+3

### **Semester III**

1.	D301	Mathematics III	3
2.	D302	Electrical Technology III	2+1
3.	D303	Workshop Technology III	2
4.	D304	Analog & Digital Electronics II	3+3
5.	D305	PCB Technology I	2+2
6.	D306	Test & Measurement I	3+1
7.	D3P1	Drawing ( Electronics )	0+1

### **Semester IV**

1.	D401	Analog & Digital Electronics III	3+2
2.	D402	PCB Technology II	2+2
3.	D403	Test & Measurement II	3+1
4.	D404	Components & Hardware	3
5.	D405	Consumer Electronics	3+2
6.	D406	Material Technology	3

### **Semester V**

1.	D501	Test & Measurement III	3+1
2.	D502	Power Electronics	3+2
3.	D503	Microprocessors	3+2
4.	D504	Product Design	3+1
5.	D505	Computer & Data Processing I	3+2
6.	D506	Costing & Management	2+0

### **Semester VI**

1.	D601	Computer & Data Processing II	3+2
2.	D602	Maintenance & Servicing	2+2
3.	D603	Project	0+10

### **2.1.8 Term Course Load**

In each semester, subject load varies from 22 to 28 credits per semester. During the course period, student has to pass certain number of subjects and

complete satisfactory the assigned project work of 10 (ten) credits in the sixth semester. On valid grounds, the authority may advise a student, who is unable to complete the course requirements in the normal period, to continue for an extra term.

### **2.1.9 Assessment**

The overall performance of a student is evaluated by assigning equal weightage to all the six semesters in order to maintain the quality of education.

A student is permitted to appear for the semester examination subject to he or she has a minimum attendance of 80% in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues. Non appearance in any examination is treated as the student having secured zero mark in that subject examination.

The evaluation is based on a average weightage system. Every subject has credit points based on the hours of study required. Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.

Every student is awarded Grade points out of maximum 8 points in each subject. (based on 8 Points Scale). Based on the Grade points obtained in each subject, Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed. For computation of SGPA CGPA, refer **ANNEXURE-III**.

### **2.1.10 Award of Diploma**

A student must complete the minimum requirement of credits in maximum period of six (6) years and must obtain a minimum CGPA of 3.5 in the course to qualify for award of Diploma. The Diploma is awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad and is also recognised by Directorate of Technical Education, Government of Maharashtra.

### **2.1.11 Scholarship**

The institute encourages and provides assistance to all the students, particularly the reserved category, to apply for the Central / State Govt. scholarship for reimbursement of tuition fee and maintenance allowance as per State Govt. rules. The tuition fee is exempted for SC/ST candidates only, subject to his/her applying for the same and fulfilling the conditions.

### **2.1.12 Assistance in Placement / Pursuing Higher Studies**

The placement wing of the Centre offers all assistance to the students for employment / self-employment. Most of the students passing out from the Centre have good opportunities to build their career.

The domicile of Maharashtra and holder of 1<sup>st</sup> Class in Diploma in Electronics Production and Maintenance (DEPM) of DOEACC Centre, (Formerly CEDTI) Aurangabad, which is equivalent to Diploma in Industrial Electronics and Diploma in Digital Electronics awarded by Maharashtra State Board of Technical Education, Mumbai, are eligible for direct admission to the second year of the Engineering Course in the state of Maharashtra. Besides, the students are encouraged to become

Student Member of professional bodies/institutions like IETE, IE, etc. during their final year of study. After passing out, they have an opportunity to pursue higher studies as a part time, being student member of such a professional institutions.

### 2.2.13 Tuition and other fees

The tuition and other fees for DEPM course are as under:

#### Course Fees per Semester for DEPM

Sr. No	Type of Fee	Category of Student	
		National	****International
01	Cost of Prospectus cum Application Form for Admission/International Students Bulletin	* Rs. 300/- ** Rs. 200/- (one time)	Rs. 2500/- (one time)
02	Admission/Eligibility Fees	Rs.300/- (one time)	Rs.12500/- (one time)
03	Tuition Fee	Rs.6000/-	Rs.30000/-
04	Examination Fee	Rs.500/-	Rs.2500/-
05	Project Fee	Rs.750/-	Rs.3750/-
06	Backlog Examination Fee Per Subject	Rs.150/-	Rs.750/-
07	Hostel Fees	Rs.3000/-	Rs.15000/-
08	***Interest Free Caution Deposit for Library	Rs.1000/- (one time)	Rs.5000/- (one time)
09	***Interest Free Caution Deposit for Hostel	Rs.1000/- (one time)	Rs.5000/- (one time)
10	Other Facility Fee (connectivity etc.)	Rs. 500/-	Rs.2500/-
11	***Interest Free Mess Deposit	Rs.1200/- (one time)	Rs.6000/- (one time)
12	Laboratory Fee per semester	Rs.500/-	Rs.2500/-
13	Library Fee per semester	Rs.200/-	Rs.1000/-
14	Cultural, Welfare Fee etc. per semester	Rs. 150/-	Rs.750/-

\*Open Category

\*\*Reserved Category (SC/ST)

\*\*\*The deposits are one time payment and to be paid at the time of admission and returnable on the completion of the course, without interest.

\*\*\*\*The above fee in foreign currency shall also be accepted.

### 2.1.14 Other information

A four weeks vocational training is arranged for DEPM students during the summer vacation at the end of IVth Semester. Attendance of this training is mandatory. An entrepreneurship Development /Awareness programme is also arranged for the final semester students.

The student is required to submit the undertaking to agree to abide by the terms and conditions of the Institute at the time of admission in the prescribed format and counter signed by parent / guardian.

The mess attached to the hostel is managed by the students themselves with

guidance from the Staff Supervisory Committee. The students are charged the actual cost of materials and services rendered by the mess staff.

All the students (boarders as well as day scholars) are required to strictly abide by the rules of the Institute / Centre, failing which disciplinary action may be taken against them.

### **2.1.15 Entrance Test for DEPM**

The written entrance test for admission to first year of Diploma in Electronics Production and Maintenance (DEPM) will be to test the candidate's basic knowledge in **Science (Physics & Chemistry) & Mathematics** subjects i.e. a candidate will be tested for his/her aptitude towards the engineering curriculum as envisaged by the Centre.

The written entrance test will be of 1½ hours duration and will be of objective type, wherein the candidate is provided, multiple choice answers and he/she is required to mark the correct answer in the same sheet, provided to him/her. In addition to above, the entrance test will also have short answer subjective type of questions and fill in the blanks. The candidates are required to bring Pen, HB Pencil, Sharpner and Eraser. Candidates are not allowed to take the Question/Answer booklet outside the exam hall.

- TEST WILL BE IN ENGLISH MEDIUM ONLY.
- CALCULATORS, MOBILE, DIGITAL DIARY, LOGBOOKS POCKET PCs ARE NOT ALLOWED IN THE EXAM HALL.
- Interview will be conducted for the candidates, short listed on the basis of the performance in written test.

## **2.2 Master of Technology in Electronics Design & Technology [M. Tech (EDT) ]**

This is a four semesters (2 years) course, which grooms students for a high profile career in design and development of electronic products. The course is approved by AICTE, New Delhi.

### **2.2.1 Eligibility**

- a) B.E. / B. Tech / B.Sc (Engineering) Degree or equivalent in Electronics / Electrical / Telecommunication / Computers / Instrumentation engineering from a recognized University with at least 55% of marks (relaxable to 50% for candidates belonging to SC / ST category). A candidate with BE / B Tech in Mechanical / Production / Industrial Engineering with exceptional aptitude and knowledge in electronics may also be considered. Those, who have appeared for engineering graduate degree and awaiting results before 30<sup>th</sup> June, 2009 are also eligible to apply.
- b) The candidate should have passed the GATE examination with valid gate score. **OR** The candidate should have been serving in Academic Institution/Industry / R&D organization engaged in electronic product or system development for at least two years and should be sponsored by the employer. He should produce necessary sponsorship certificate alongwith application in

the prescribed form. **OR** Foreign nationals having equivalent degree of a recognised university. Foreign nationals should apply through proper channel.

- c) Non – GATE candidate may also apply but their application will be considered only if GATE candidates are not available and the seats are vacant.

**2.2.2 Intake** 30 (10 + 5 + 3 + 2 + 5 + 5)

**2.2.3 Categories** (Non-sponsored (Open/OBC/SC/ST and PD), Foreign National and Sponsored)

### **2.2.4 Selection**

#### **(A) National Applicants**

Candidates will be short listed based on the minimum eligibility criteria. The selection for admission to M. Tech (EDT) program, from the short listed candidates will be based on their performance in an written entrance test and interview to be conducted in the month of July/August. Seats are reserved for candidates belonging to reserved categories as per Government of India Rules and AICTE, New Delhi Approval.

Provisional admission will be granted on the basis of order of merit of the candidate as per the category-wise merit list, prepared on the basis of performance of candidates in written test and interview. As per admission norms, the admission process continues till approved intake of thirty (30) candidates are admitted to the M. Tech Course by operating the category-wise main (selected) list and then, the category-wise waiting list, if required or a time limit decided by the competent authority is over, whichever earlier. The category-wise main (selected) list and waiting list of the candidates for admission to 19<sup>th</sup> batch (2009-10) of M. Tech Course will be displayed on the Notice Board of this institute. The date and time for operation of the waiting list shall also be declared alongwith the list of selected candidates. All the waitlisted candidates should make themselves available at the time of operation of the waiting list, otherwise their claim shall be forfeited. The waitlisted candidates, available at the time of operation of the waiting list, shall be provisionally admitted as per the merit of the category-wise waiting list. The selected main and waiting list candidates are required to register on the day as notified alongwith the list displayed by making payment as mentioned in section 2.2.12, otherwise their claim shall be forfeited. Admission process of the M. Tech Course is completed when the approved intake of thirty (30) candidates are provisionally admitted and registered or a time limit decided by the competent authority is over, whichever is earlier.

The GATE candidates will be considered first as per the above process. Non-GATE candidates will be considered against the category-wise vacancies as per the above process, if GATE candidates are not available.

#### **Admission in the event of cancellation of secured admission by a student is as follows:**

If any vacancy arises due to leaving the course by a registered student after completion of admission process, the vacancy may be filled on case to case basis at the discretion of the Director as the admission process is completed, by following below mentioned procedure:

- i) The students from the selected 25 candidates, who could not reach the Centre for admission on the preintimated day because of legal and/or genuine reason (s) and approaching / contacting the institute are first considered for filling the said vacancy.
- ii) After (i), the candidates, who have not been offered the admission and approaching, shall be considered.
- iii) Then the candidates, as per order of merit shall be informed about the offering of the admission.
- iv) In i, ii & iii, the preference shall be given to GATE candidate(s) over Non-GATE candidate(s) and ranking in the merit list.

### **B) International Applicants (Foreign Nationals)**

The seats are reserved for foreign nationals as per Government of India Rules and University approval. Admission of foreign nationals is subject to the guidelines, laid down by Government of India from time to time. Foreign nationals may apply for admission to M. Tech course, subject to fulfilling the minimum eligibility requirements through proper channel. Their application will, however, be considered separately on first cum first serve basis as per procedure, mentioned in **ANNEXURE-I**.

Foreign nationals are required to obtain International Student Bulletin, containing the application form for eligibility cum admission **ANNEXURE-I(A)** and Declaration and Undertaking format (**ANNEXURE-I(B)**) on payment of Rs.2500/- or equivalent foreign currency. Further, they are required to submit the duly filled eligibility cum admission form alongwith demand draft of the prescribed eligibility fee and the required supporting documents to DOEACC Centre, Aurangabad, Maharashtra (India).

### **2.2.5 Academic Calendar – Refer ANNEXURE – II**

### **2.2.6 Scheme of Instruction**

Every student has to register for the core subjects and electives, relevant to his/her course at the start of the semester in consultation with the concerned faculty / Academic Section.

### **Semester I (Autumn)**

<b>Sr. No.</b>	<b>Code No.</b>	<b>Subject</b>	<b>Credit</b>
1.	M1001	Industrial Design of Electronic Equipment	3
2.	M1002	Microcomputer System Design	3
3.	M1003	Analog & Digital Systems	3
4.	---	Elective - 1	3
5.	---	Elective - 2	3
6.	---	Elective - 3	3
<b>Electives</b>			
1.	M1004	Power Electronics	
2.	M1005	Instrumentation & Process Control	
3.	M1006	Software Engineering -I	
4.	M1008	Opto Electronics Devices & Instruments	

## **Semester II (Spring)**

1.	M2001	Advanced Microcomputer System Design	3
2.	M2002	Physical Design of Electronic Equipments	3
3.	---	Elective - 4	3
4.	---	Elective - 5	3
5.	---	Elective - 6	3
6.	M3001	Project Feasibility Seminar	1

### ***Electives***

1.	M2003	Agri-Instrumentation & Control
2.	M2004	Design of Power Electronic Equipment
3.	M2006	Software Engineering - II
4.	M2008	Design of Opto-electronic Systems

## **Semester III (Autumn)**

1.	M3002	Project	14
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## **Semester IV (Spring)**

1.	M3002	Project	14
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### **2.2.7 Term Course Load**

The number of credits a student is required to take in the first and second semesters are 18 and 16 respectively. In the first semester, 9 credits relates to core subjects and 9 to electives. Whereas in the second semester 6 credits to core subjects and 9 to electives. The electives are selected by the students in consultation with the concerned faculty / Academic Section. The third and fourth semesters are entirely devoted to Project work and carry total credit points of 28. During the course period, student has to pass certain number, of subjects and complete satisfactorily the assigned project work of 28 (Twenty eight) credits in the third and the fourth semesters. On valid grounds, the authority may advise a student, who is unable to complete the course requirement in the normal period, to continue for an extra term.

### **2.2.8 Assessment**

The overall performance of a student is evaluated by assigning equal weightage to all the four semesters in order to maintain the quality of education.

A student is permitted to appear for the semester examination subject to he or she has a minimum attendance of 75% in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues. Non appearance in any examination is treated as the student having secured zero mark in that subject examination.

The evaluation is based on a average weightage system. Every subject has credit points based on the hours of study required. Every student is assessed in a subject with equal weightage to sessional work and semester examination. There by making the students study regularly.

Every student is awarded Grade points out of maximum 8 points in each subject (based on 8 Points Scale). Based on the Grade Points obtained in each subject, Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed. For computation of SGPA and CGPA, refer **ANNEXURE – III**

## 2.2.9 Award of Degree

A student must complete the minimum requirement of credits in maximum period of four 4 years and must obtain a minimum CGPA of 4.0 in the course to qualify for award of Degree. The PG Degree is awarded by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS).

## 2.2.10 Scholarship

Non-sponsored students, admitted to the M Tech (EDT) course, provided they have passed the GATE examination, are eligible for the scholarship of Rs. 8000/- per month, subject to sanction and receipt of the said amount from AICTE New Delhi. Since academic session 2002-03, the Institute is applying and receiving Grant-in-Aid for Scholarship to GATE passed & non-sponsored students from AICTE, New Delhi, the scholarship amount shall be reimbursed to the students on the receipt of the grant from AICTE, New Delhi by the institute. The award of scholarship and its continuation is subject to regular attendance, satisfactory progress, good conduct and abiding by the rules of the Institute. Sponsored or Non-GATE students are not entitled for the said scholarship.

The tuition fee is exempted for SC/ST candidates only, subject to his/her applying for the same and fulfilling the conditions.

It is obligatory for every student, granted admission to M. Tech (EDT) and awarded scholarship, to undertake at least 10 hours per week of work related to teaching and research activities as assigned to him / her

## 2.2.11 Assistance in Placement

The placement wing of the Centre offers all assistance to the students interested in securing an appropriate employment. The students of this course have been well received by the Industries.

## 2.2.12 Tuition and Other Fees

The tuition and other fees for M.Tech (EDT) Course are as under:

### Course Fees per Semester for M. Tech (EDT)

Sr. No	Type of Fee	Category of Student	
		National	****International
01	Cost of Prospectus cum Application Form for Admission/International Student Bulletin	* Rs.500/- ** Rs.300/- (one time)	Rs. 2500/- (one time)
02	Admission/Eligibility Fee	Rs. 500/- (one time)	Rs.15000/- (one time)
03	Tuition Fee	Rs.15000/-	Rs.45000/-

04	Examination Fee	Rs.1000/-	Rs.5000/-
05	Project Fee	Rs.1000/-	Rs.5000/-
06	Backlog Examination Fee Per Subject	Rs.250/-	Rs.1250/-
07	Hostel Fees	Rs.4000/-	Rs.20000/-
08	*** Interest Free Caution Deposit for Library	Rs.1000/- (one time)	Rs.5000/- (one time)
09	*** Interest Free Caution Deposit for Hostel	Rs.1000/- (one time)	Rs.5000/- (one time)
10	Other Facility Fee (connectivity etc.)	Rs.1000/-	Rs.5000/-
11	*** Interest Free Mess Deposit	Rs.1200/- (one time)	Rs.6000/- (one time)
12	Laboratory Fee per semester	Rs.750/-	Rs.3750/-
13	Library Fee per semester	Rs.300/-	Rs.1500/-
14	Cultural, Welfare Fee etc. per semester	Rs.150/-	Rs.750/-

\* Open Category

\*\* Reserved Category (SC/ST)

\*\*\* The deposits are one time payment and to be paid at the time of admission and returnable on the completion of the course without interest.

\*\*\*\* The above fee in foreign currency shall also be accepted.

### 2.2.13 Other Information

The student is required to submit the undertaking to agree to abide by the terms and conditions of the institute and AICTE New Delhi at the time of admission in the prescribed format.

The students are encouraged to interact with industry for getting exposed to industry environment and provided all support and motivated to select & undertake real problems of industry as their project.

The mess attached to the hostel is managed by the students themselves with guidance from the Staff Supervisory Committee. The students are charged the actual cost of materials and services rendered by the mess staff.

All the students (boarders as well as day scholars) are required to strictly abide by the rules of the Institute / Centre, failing which disciplinary action may be taken against them.

### 2.2.14 Entrance Test for M. Tech (EDT)

The written entrance test for admission to first year of M. Tech (EDT) of DOEACC Centre (formerly CEDTI), Aurangabad will be to test the following:

1. Basic aptitude of the candidate towards the Design and Technology.
2. Knowledge of the candidate in Electronics, Electrical Engineering, Computer Science / IT and allied subjects.

The written entrance test will be of 1½ hours duration and will be of objective type, wherein the candidate is provided multiple choice answers and he/she is required to mark the correct answer in the same sheet, provided to him/her. In addition to above, the test may also have short answer subjective type of questions and fill in the blanks. The candidates are required to bring Pen, HB Pencil, Sharpner and Eraser.

Candidates are not allowed to take the Question/Answer booklet outside the examination hall.

- TEST WILL BE IN ENGLISH MEDIUM ONLY.
- CALCULATORS, MOBILE, DIGITAL DIARY, POCKET PCs, MOBILE PHONE, LOG BOOKS ARE NOT ALLOWED IN THE EXAMINATION HALL.
- Interview will be conducted for the candidates, short listed on the basis of performance in written test.

### **2.3 Doctoral (Ph.D) Program in Engineering & Technology.**

DOEACC Centre Aurangabad is a recognized **Research Centre** of the Dr.B.A.M. University, Aurangabad (MS) for conducting research leading to Ph.D Degree in Engineering and Technology. The institute has the facility to pursue the research work in the fields of Electrical, Electronics, Instrumentation, Communication, Information Technology/Science, Computer Engineering/Science, etc.

Interested candidates, meeting the eligibility criteria set by the Dr.B.A.M. University, Aurangabad or any other University/Deemed University proposed by the candidate, shall be allowed to propose this institute as **Research Centre**, subject to the Rules and Regulations of this institute, laid down for this purpose.

The candidate shall register with the proposed University/Deemed University and complete all the formalities, laid down by the said University from time to time (Fee, Progress Report, Synopsis, Thessis, etc.), in addition to fulfilling the requirements of this institute.

Separate Prospects for Doctoral (Ph.D) Program is available in the institute on payment of Rs. 500/- (Rs.300/- for SC/ST/PD candidates) throughout the year. Interested candidates may contact Academic Co-ordinator, DOEACC Centre, Aurangabad (MS) for further details and/or for obtaining the Prospectus for Ph.D Program.

### **3.0 Reservation**

As per Government of India's Policy, there is a reservation for persons, belonging to socially and educationally backward classes and persons with Disabilities (Physically Handicapped Persons) in education. All reservations are not of the same nature. There are two types of reservations, which may, for the sake of convenience, be referred to as "Vertical Reservations" and "Horizontal Reservations". The reservations in favor of Scheduled Caste, Scheduled Tribes and Other Backward Classes (OBCs), made under article 16 (4), may be called vertical reservations whereas reservations in favour of physically handicapped, made under clause (1) of Article 16, can be referred as horizontal reservations, horizontal reservations cut across the vertical reservations, called as "interlocking reservation".

### **3.1 Percentage of Reservation**

As per Government of India Rules, there is a reservation of 27% of seats for the OBCs, 15% of seats for the SCs and 7.5% of seats for the STs in admission to various courses. Further there is a reservation of at least 3% of seats for Persons with Disabilities (PDs), as per section 39 of the Persons with Disabilities (Equal opportunities, Protection of Rights and Full Participation) Act, 1995. Since the reservation for PDs the horizontal reservations, is interlocking reservations, the

candidate selected against this quota will be placed in the appropriate category, that is, if the candidate belongs to SC/ST category, he/she will be placed in that quota by making necessary adjustment. Similarly, if the candidate belongs to General/Open category, he/she will be placed in that category/quota by making necessary adjustment.

The reservation for the OBCs for PG Engineering, Course is implemented as per the directives of Ministry of Human Resource Development (MHRD), Govt. of India for Central Educational Institutions (CEIs), vide OM NO. 1-1/2005-U.1A/847 dated 20<sup>th</sup> April, 2008, and AICTE, New Delhi approval for the seats of OPEN, OBC, SC & ST categories for PG Engineering Courses.

### **3.2 Relaxation in Age**

The relaxation in upper age limit is two (2) years for SC/ST candidates and five (5) years for Persons with Disabilities (PD) candidates, applying for DEPM course.

### **3.3 Relaxation in Qualifying Marks**

The relaxation in qualifying marks is 5% for SC & ST candidates only.

### **3.4 Relaxation in cost of Prospectus cum Application Form**

The relaxation in cost of Prospectus cum Application Form is Rs 100/- for DEPM Course and Rs. 200/- for M. Tech. Course for SC, ST, & PD Candidates.

### **4.0 Guidelines for Filling the Application Form**

1. The candidates are requested to fill-in the application form carefully in block letters. Use Blue ink preferably.
2. Write the course applying for at Sr. No. 1.
3. Write all the details asked in the Admit Card and Acknowledgement Slip.
4. Signature of Parent is must for all candidates applying for DEPM course.
5. Reserved category candidates (OBC/SC/ST) are required to submit the Caste Certificate alongwith the declaration/undertaking as per the format enclosed in **ANNEXURE-IV (A)/IV (B)** or an attested photocopy of the caste certificate issued by the competent authority, as mentioned therein, should be enclosed.
6. Physically Handicapped Candidates are required to submit Physical Disability Certificate as per the format enclosed in **ANNEXURE-V**.
7. 10<sup>th</sup> Standard (SSC) mark sheet or certificate shall only be recognized as the proof for date of birth. No other certificate / document shall be considered.
8. A sponsorship letter is to be submitted as per the format enclosed in **ANNEXURE-VI (A)** in case of sponsored candidate for M Tech. course.
9. A studentship certificate is to be submitted as per the format enclosed in **ANNEXURE-VII**, if the candidate appearing in their final year of graduation or X<sup>th</sup> class as the case be and who is applying for M. Tech. / DEPM course.
10. The candidates are required to produce their original certificates at the time of interview.

11. Selected candidates will have to submit all original certificates at the time of registration for the course.
12. Selected candidates are required to pay the fees as mentioned in the prospectus / call letter immediately on declaration of the selected list at the time of registration otherwise the claim of the candidate for the seat will be null & void (forfeited).

## 5.0 List of Staff Members

Sr.	Name	Education/Qualification	Designation	Area of working
1.	Dr. V. N. Walivadekar	PhD (Ele. Engg)	Director	Power Electronics, A & D Design
2.	Dr. M.P. Pillai	PhD (Physics), M Tech (Laser & EO Engg)	AD	Opto, E-Learning
3.	Shri. Daniel A. Figueiredo	M.Tech. (EDT)	P D E	Consumer Electronics, A&D Electronics
4.	Shri. S.T. Valunjkar	M Tech (Ele. Industries)	P D E	Power Electronics, Agri-electronics,
5.	Shri. V. Krishnamurthy	M Tech (EDT)	P D E	IT Security, Software Engg., Electronics Mfg. Industrial & Physical Design of Electronic Equipment, Product Design.
6.	Shri. R. R. Deshpande	M Tech (EDT)	P D E	Embedded Systems, $\mu$ P / $\mu$ C, T&M
7.	Shri. Yashpal Gogia	M Tech (Opto Ele. & Fiber Optics)	S D E	Optoelectronics, DSP
8.	Shri. D. S. Raje	B. E (Elect.)	S D E	Communications, T&M
9.	Ms. Warsha Kandlikar	M.E (Elect. & Tele.)	S D E	VLSI Design, $\mu$ P / $\mu$ C Agri-Electronics
10.	Shri. Y. A. Khan	MCA	Sr. Progr. (SG-I)	RDBMS, OS, Oracle, C Language
11.	Ms. P. D. Bharne	DETE (Ele. & Telecom)	Dy. Eng	VLSI Design, PCB & Electronics
12.	Ms. V. S. Jahagirdar	M Tech (EDT)	Dy. Eng	Embedded Systems, A&D Electronics, $\mu$ P / $\mu$ C

### Technical Support Staff

13.	Shri. A.N. Rasal	M. Lib, SC.ADCSSAA	Librarian (SG)	Lib. Management
14.	Shri. M. S. Kshirsagar	DIE (Indl. Elect)	Dy. Eng	Microcontroller, A & D Electronics
15.	Shri. B. B. Sorte	DME, Diploma in Comp. Science & Management	Dy. Eng	CAD / CAM, E-learning, Workshop Technology, Engineering Drawing
16.	Shri. Keshav Vaishy	DCE	Dy. Eng (Civil)	Engg. Drawing, Civil Construction & Mainte.
17.	Shri. M. L. Garud	DIEE (Indl. Elect)	Dy. Eng	A&D Electronics
18.	Shri. M. S. Chawhan	M.Sc. B. Lib Science	Librarian	Lib. Management
19.	Shri. S. G. Wankhede	ITI (Elect)	Sr. L Asst (Gr-I)	A & D Electronics
20.	Ms. S. R. Jape	B. F. A. (App. Art)	Sr. L Asst (Gr-I)	Multimedia, Fine Arts, Product Design

21. Shri. M. Mahto	DEE (Elect. Engg.)	Sr. L Asst (Gr-I) A & D Electronics
22. Shri. M. K. Kulkarni	ITI (Fitter)	Sr. W Asst Workshop Tech.
23. Shri. R. N. Potbhare	ITI (Welder)	W Asst Workshop Tech.
24. Shri. S. S. Paropkari	ITI (electrician)	Sr. Elect Elec. Maintenance

## **Administrative Staff**

25 Shri. B N Choudhury	Admn. cum Finance Officer
26. Shri. V. Chandola	Finance & Acct. Officer Gr. I
27. Shri. M. J. Quazi	Admn. Officer Gr. I
28. Shri. S. T. Hosurkar	Sr. PA to Director
29. Ms. V. D. Deshmukh	Asst. Admn. Officer
30. Shri. S. N. Agawane	Asst. Accounts Officer
31. Shri. U. M. Joshi	Sr. Personal Assistant
32. Shri. S. R. Bhoge	Asst. Admn. Officer
33. Shri. A. K. Zade	Office Assistant Gr. II
34. Shri. R. D. Muley	Office Assistant Gr. III
35 Shri. Maroti Hamand	Office Assistant Gr. III
36. Shri. P. A. Sonne	Office Attendant Gr. I
37. Shri. B. D. Tigote	Office Attendant Gr. I
38. Shri. S. P. Kakarwal	Security Guard Gr. II
39. Shri. M. T. Waidande	Office Attendant Gr. II
40 Shri. Vasant Jadhav	Safaiwala

## DOEACC Centre, Aurangabad, Maharashtra (India)

### ANNEXURE-I

#### Admission Procedure For Foreign Nationals

<b>Step-1</b>	Purchase the International Student Bulletin / Prospectus (Cost: Rs.2500/- or equivalent hard Foreign Currency), containing the application form for eligibility cum admission to DEPM /M.Tech /Ph.D from the DOEACC Centre, Aurangabad-431004, Maharashtra (India). The form may also be downloaded from the website. In that case, the above cost may be paid alongwith the duly filled application form.
<b>Step-2</b>	Eligibility fees for Foreign Student will be Rs. 12,500/- for DEPM and Rs.15, 000/- for M.Tech (EDT)/ Ph.D. This may be paid by crossed Demand Draft in favor of Director DOEACC Centre, Aurangabad-431004, Maharashtra (India), payable at Aurangabad (Maharashtra), on the registered address of DOEACC Centre, Aurangabd-431004, Maharashtra (India).
<b>Step-3</b>	Fill-up the application form for eligibility cum admission ( <b>Annexure-I (A)</b> ) and submit it alongwith necessary documents. The counterfoil of the Eligibility fees must also be submitted. Every foreign student will be required to submit Declaration and Undertaking on Rs. 100/- stamp paper. (The format of Declaration and Undertaking is enclosed as ( <b>Annexure-I (B)</b> )).
<b>Step-4</b>	The DOEACC Centre, Aurangabad Maharashtra (India), will require (05) working days for processing the eligibility cum admission form of DEPM/M.Tech (EDT)/Ph.D after submission of all required documents to the office.
<b>Step-5</b>	If selected, Centre will dispatch Provisional Admission & Eligibility letter to you. On the basis of this letter, the Indian Embassy in your country will issue a Student Visa (Only Student Visa is acceptable). If mentioned in the Provisional Admission letter, please pay the Registration fee, Medical fee & Medical Insurance fee of <b>USD300</b> through the appropriate Bank challan. This may be paid in <b>USD</b> or the equivalent sum in <b>Indian Rupees</b> . This must be done by the applicant in person. The applicant must carry all original certificates, which must be shown on demand.
<b>Step-6</b>	Please submit receipt of the above payments to the DOEACC Centre, Aurangabad, Maharashtra (India), This completes the initial procedure.
<b>Step-7</b>	The DOEACC Centre, Aurangabad, upon receipt of above documents and fees, will require minimum (07) seven working days to finally process for admission on a first come first basis. On completion of Seven (07) working days, please contact, in person, to collect your provisional admission letter.
<b>Step-8</b>	Please pay the fees at the DOEACC Centre, Aurangabad and complete the admission formalities.
<b>Step-9</b>	Foreign National is required to report to DOEACC Centre, Aurangabad within one month of the date of issue of Provisional Admission & Eligibility letter or before the start of forthcoming Academic Year. Failure to report will entail automatic cancellation of admission. All the fees paid to DOEACC Centre, Aurangabad shall be forfeited (not refunded).
<b>Step-10</b>	It is mandatory for any student from non-english speaking countries to pass English Proficiency Test and it is his / her responsibility to be present on the day of the examination at the location, specified by DOEACC Centre, Aurangabad, Maharashtra (India.) Following students are exempted from English Proficiency Test. <ul style="list-style-type: none"><li>• Students from english speaking countries.</li><li>• Students, who have passed from Indian H.S.C. Boards.</li><li>• Students, who have passed ELICIS Course.</li></ul>
<b>Step-11</b>	According to circular of Govt. of India, issued by Ministry of Human Resource Department, Universities and Colleges must not grant admission to foreign students, if they do not have student or provisional student visa, endorsed on their passport.

The foreign students are required to strictly adhere to the above procedure. Students, not following the above rules, will not be granted admission.

**DOEACC Centre, Aurangabad, Maharashtra (India)****ANNEXURE- I (A)****Application Form for Eligibility Cum Admission to DEPM / M.Tech (EDT) for Foreign Nationals**

To,  
 The Director,  
 DOEACC Centre Aurangabad,  
 University Campus, Aurangabad  
 4310004 (MS) India  
 Office Telephone: + 91(0240) 2400120, + 91(0240) 24000121Ext: 219, 233, 242  
 Admn-cum-fin. Officer Tel: + 91(0240) 2400047, Office Fax No.+ 91(0240) 2400051  
 Telegram: DOEACC Centre, University campus, Aurangabad.  
 Website: [www.doeaccaurangabad.org.in](http://www.doeaccaurangabad.org.in), [www.doeacc.edu.in](http://www.doeacc.edu.in)  
 E-mail: [academics@doeaccaurangabad.org.in](mailto:academics@doeaccaurangabad.org.in)

**Important Instructions**

1. Form should be signed by the student.
2. Incomplete form will not be accepted.
3. No refund of Fees.
4. This form is valid only for the Academic year of application.

Sir/Madam,

I hereby apply for grant of eligibility and admission as an International Student to Diploma in Electronics Production and Maintenance (DEPM) / M.Tech (EDT) / Ph.D Degree Course during the Academic year \_\_\_\_\_ and request you to kindly grant me a certificate of eligibility and admission to the said course in DOEACC Centre, Aurangabad, Maharashtra (India). I submit my particular as under:

**Course Applied:** \_\_\_\_\_

Passport Size  
 Recent  
 Photograph  
 Attested  
 (Size:  
 4.5x5.5 cm)

**CANDIDATE'S DETAILS**

**Last Name** : \_\_\_\_\_  
**First Name** : \_\_\_\_\_  
**Middle Name** : \_\_\_\_\_  
**Mother Name** : \_\_\_\_\_  
**Date of Birth** : DD/MM/ YYYY    **Nationality:** \_\_\_\_\_

**ADDRESS FOR CORRESPONDENCE**

ADDRESS FOR CORRESPONDENCE										
Pin:										



# **DOEACC Centre, Aurangabad, Maharashtra (India)**

## **ANNEXURE-I (B)**

**(To be typed on Rs. 100/- Stamp Paper)**

### **Declaration and Undertaking**

I hereby declare that I have carefully read this application form for eligibility and admission and have noted the instructions / requirements thereby. I have also carefully noted the rules of eligibility & conduct and discipline, laid down by the DOEACC Centre, Aurangabad and I agree to abide by them. I understand and declare that I shall be responsible for any discrepancies, error, wrong or incorrect information, supplied by me in this application form and for cancellation of admission thereby or otherwise found ineligible. I undertake to furnish the necessary certificate(s) / document(s) / paper(s) in original alongwith a true copy of each of them as and when asked for, failing which I understand that my eligibility and admission stands automatically cancelled and that the DOEACC Centre, Aurangabad is not responsible for the same.

I hereby declare that the information furnished by me in this form is true and correct to the best of my knowledge. I am liable to be disqualified if the competent authority notices that I have furnished any false information.

Yours Faithfully,

(Name & Signature of Foreign National)

Date: dd/mm /yyyy

Place:

## ANNEXURE-II

### Academic Calendar for M.Tech (EDT) and DEPM Programs For the Year 2009-2010

Sr. No.	Semester-I (Autumn)	DEPM	M.Tech (EDT)
1.	Start of Registration	28 <sup>th</sup> July, 09 (D)	03 <sup>rd</sup> Aug, 09 (M)
2.	Last date of Registration (For 1 <sup>st</sup> year students)	29 <sup>th</sup> July, 09 (D)	07 <sup>th</sup> July, 09 (M)
3.	Instruction Begins	29 <sup>th</sup> July, 09 (D)	03 <sup>rd</sup> Aug, 09 (M)
4.	Class Test & Project Assessment-I (M.Tech-II)	31 <sup>st</sup> Aug, to 04 <sup>th</sup> Sept, 2009	
5.	Cultural Festival	14 <sup>th</sup> to 19 <sup>th</sup> Sep, 2009	
6.	Class Test 2 & Project Assessment-I (M.Tech-II)	26 <sup>th</sup> to 30 <sup>th</sup> Oct, 2009	
7.	Allotment of Project to DEPM Students	09 <sup>th</sup> to 13 <sup>th</sup> Nov, 2009	
8.	Instruction Ends	27 <sup>th</sup> Nov, 2009	
9.	Semester (Practical) Examination	30 <sup>th</sup> Nov, 2009 to 04 <sup>th</sup> Dec, 2009	
10.	Semester (Theory) Examination	07 <sup>th</sup> to 19 <sup>th</sup> Dec, 2009	
11.	Semester Break (DEPM)	20 <sup>th</sup> Dec, 2009 to 03 <sup>rd</sup> Jan, 2010	
12.	Declaration of Results	01 <sup>st</sup> Jan, 2010	
<b>Semester-II (Spring)</b>			
13.	Start of Registration	04 <sup>th</sup> Jan, 2010	
14.	Last date of Registration	08 <sup>th</sup> Jan, 2010	
15.	Instruction Begins	04 <sup>th</sup> Jan, 2010	
16.	Class Test -1 (M.Tech-I / DEPM) & Project Assessment-III (M.Tech-II)	01 <sup>st</sup> to 05 <sup>th</sup> Feb, 2010	
17.	Winter Sports	22 <sup>nd</sup> to 26 <sup>th</sup> Feb, 2010	
18.	Class Test-2 (M.Tech / DEPM) & Project Assessment-IV (M.Tech-II)	15 <sup>th</sup> to 19 <sup>th</sup> Mar, 2010	
19.	Allotment of Project to M.Tech 1 <sup>st</sup> year students	05 <sup>th</sup> to 09 <sup>th</sup> April, 2010	
20.	Instruction Ends	07 <sup>th</sup> May, 2010	
21.	Semester (Practical) Examination	10 <sup>th</sup> to 14 <sup>th</sup> May, 2010	
22.	Semester (Theory) Examination	17 <sup>th</sup> to 29 <sup>th</sup> May, 2010	
23.	Project Assessment (Final) (M.Tech-II / DEPM) Project Feasibility Seminar (M.Tech-I)	31 <sup>st</sup> May, to 04 <sup>th</sup> June, 2010	
24.	Semester Break (DEPM)	30 <sup>th</sup> May, to 11 <sup>th</sup> July, 2010	
25.	Declaration of Results	18 <sup>th</sup> June, 2010	

\* The date for issue and submission of form of GOI Scholarship and Freeship for all reserve category students and issue and submission of Option form for exemption in Tuition fees for SC & ST students under Govt. of India Scheme through DOEACC Society is from 03<sup>rd</sup> to 14<sup>th</sup> Aug, 2009.

## ANNEXURE – III

### Computation of SGPA & CGPA

#### Assessment

The overall performance of a student is evaluated by assigning equal weightage to all the semesters (six in case of DEPM and four in case of M. Tech) in order to maintain the quality of education.

A student is permitted to appear for the semester examination, subject to he or she has a minimum attendance of 80% for DEPM or 75% for M. Tech in theory and practical classes, completes all his/her sessional assignments and clears all his/her dues. Non appearance in any examination is treated as the student having secured zero mark in that subject examination.

The evaluation is based on a average weightage system. Every subject has credit points based on the hours of study required. Every student is assessed in a subject with equal weightage to sessional work and semester examination, thereby making the students study regularly.

Every student is awarded points out of maximum 8 points in each subject. (based on 8 Points Scale). Based on the Grade points obtained in each subject the Semester Grade Point Average (SGPA) and then Cumulative Grade Point Average (CGPA) are computed. The computation of SGPA & CGPA, is as under:

Semester Grade Point Average (SGPA) is the weighted average of Grade Points obtained by a student in a semester and is computed as follows:

$$\text{SGPA} = \frac{U_1 \times M_1 + U_2 \times M_2 + \dots + U_n \times M_n}{U_1 + U_2 + \dots + U_n}$$

Where  $U_1, U_2, \dots$  are subject credit of the respective course and  $M_1, M_2, \dots$  are the marks obtained in the respective subject out of 8 (Grade Point)

The Semester Grade Point Average (SGPA) for all the six semester is also mentioned at the end of every semester.

The Cumulative Grade Point Average (CGPA) is used to describe the overall performance of a student in the Diploma / Degree course and is computed as under:

$$\text{CGPA} = \frac{\sum_{n=1}^{6/4} \text{SGPA}(n) C_n}{\sum_{n=1}^{6/4} C_n}$$

Where  $\text{SGPA}(n)$  is the  $n^{\text{th}}$  Semester SGPA of the student and  $C_n$  is the  $n^{\text{th}}$  Semester total credit. The maximum value of  $n$  is six (6) in case of DEPM Program and four (4) in case of M. Tech Program.

The Semester and Cumulative GPA are rounded off to the second place of decimal.

## **ANNEXURE- IV (A)**

### **Format of Caste Certificate (For SC/ST Candidates Only)**

The authorities empowered to issue this certificate are:

1. District Magistrate / Additional District Magistrate / Collector / Deputy Commissioner / Addl. Deputy Commissioner / Deputy Collector / Class I Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Extra Asst. Commissioner (not below the rank of I<sup>st</sup> class stipendiary Magistrate)
2. Chief Presidency Magistrate / Addl. Chief Presidency Magistrate / Presidency Magistrate
3. Revenue Officer not below the rank of Tahsildar.
4. Sub-divisional officer of the area, where the candidate and / or his family normally resides.
5. Administrator / Secretary to Administrator / Development Officer at Lakshadweep Islands.

Certified that Mr./Ms. \_\_\_\_\_  
son/daughter/wife of \_\_\_\_\_ of Village / Taluka  
\_\_\_\_\_ district \_\_\_\_\_ state \_\_\_\_\_ and  
ordinarily residing at Village / Taluka \_\_\_\_\_ district  
\_\_\_\_\_ state \_\_\_\_\_, an applicant appearing for  
entrance test of DEPM/M. Tech. (EDT) of DOEACC Centre, Aurangabad, belongs to  
Scheduled Caste/Tribe.

Signature \_\_\_\_\_

Name & Designation \_\_\_\_\_

Office Seal

Date:

Place:

\_\_\_\_\_  
*(Note: The above certificate format is suggestive only. The candidate is required to prepare and submit it on separate sheet.)*

## **ANNEXURE- IV (B)**

### **Format of Caste Certificate (For OBC Candidates Only)**

This is to certify that Shri/Smt./Kum. \_\_\_\_\_  
Son/Daughter of Shri/Smt. \_\_\_\_\_ of  
Village/Town \_\_\_\_\_  
District/Division \_\_\_\_\_ in  
the State of \_\_\_\_\_  
belongs to the \_\_\_\_\_  
Community, which is recognized as a backward class under:

- (i) Resolution No. 12011/68/93-BCC(C) dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186 dated 13/09/93.
- (ii) Resolution No. 12011/09/94-BCC dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163 dated 20/10/94.
- (iii) Resolution No. 12011/7/95-BCC dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88 dated 25/05/95.
- (iv) Resolution No. 12011/96/94 BCC- dated 09/03/96.
- (v) Resolution No. 12011/44/96 BCC dated 06/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 11/12/96.
- (vi) Resolution No. 12011/13/97- BCC dated 03/12/97.
- (vii) Resolution No. 12011/99/94- BCC dated 11/12/97.
- (viii) Resolution No. 12011/68/98-BCC dated 27/10/99.
- (ix) Resolution No. 12011/88/98-BCC dated 06/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270 dated 06/12/99.
- (x) Resolution No. 12011/36/99-BCC dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71 dated 04/04/2000.
- (xi) Resolution No. 12011/44/99-BCC dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 21/09/2000.
- (xii) Resolution No. 12015/09/2000-BCC dated 06/09/2001.
- (xiii) Resolution No. 12011/01/2001-BCC dated 19/06/2003.
- (xiv) Resolution No. 12011/04/2002-BCC dated 13/01/2004.
- (xv) Resolution No. 12011/09/2004-BCC dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 16/01/2006.

Shri./Smt./Kum. \_\_\_\_\_  
and/or his family ordinarily reside(s) in the \_\_\_\_\_  
District/Division of the State of \_\_\_\_\_

This is also to certify that he/she does not belong to the persons/sections (Creamy Layer) mentioned in Column 3 of the Schedule to the Government of India, Department of Personnel & Training O. M. No. 36012/22/93-Estt.(SCT) dated 08/09/93, which is modified vide OM No. 36033/3/2004 Estt. (Res.) dated 09/03/2004.

District Magistrate/  
Deputy Commissioner, etc.

Seal

Dated:

Place:

**NOTE:**

- (a) The term 'Ordinarily' used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.
- (b) The authorities competent to issue Caste Certificates are indicated below:
- (i) District Magistrate / Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / Ist Class Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Executive Magistrate/ Extra Assistant Commissioner (not below the rank of Ist Class Stipendiary Magistrate).
  - (ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.
  - (iii) Revenue Officer not below the rank of Tahsildar and
  - (iv) Sub-Divisional Officer of the area, where the candidate and / or his family resides.

**Declaration/Undertaking – for OBC Candidates only**

I, \_\_\_\_\_ son/daughter  
of Shri/Smt. \_\_\_\_\_ resident  
of village/town/city \_\_\_\_\_  
district \_\_\_\_\_ in  
the State of \_\_\_\_\_ hereby  
declare that I belong to the community \_\_\_\_\_ which is  
recognised as a backward class by the Government of India for the purpose of reservation in  
services as per orders contained in Department of Personnel and Training Office  
Memorandum No. 36012/22/93-Estt. (SCT), dated 8/9/1993. It is also declared that I do not  
belong to persons/sections (Creamy Layer) mentioned in Column 3 of the Schedule to the  
above referred Office Memorandum, dated 8/9/1993, which is modified vide Department of  
Personnel and Training Office Memorandum No. 36033/3/2004 Estt. (Res.) dated 9/3/2004.

Signature of the Candidate

Place:

Date:

***NB : Declaration/undertaking not signed by Candidate will be rejected***

**Creamy Layer Definition**

OBC Creamy layer is defined comprehensively at <http://ncbc.nic.in/html/creamyayer.html> All candidates for the OBC reserved seats should make sure that they do not satisfy any of the creamy layer criteria as listed in the website. Some general exclusion for quick reference (no way comprehensive) are as follows.

1. Any one of the parents holds a constitutional position in Govt. of India.
2. Any one of the parents is a class I officer.
3. Both the parents are class II officers.
4. Any one of the parents is employed in an equivalent rank to class I officer or both parents equivalent to class II officer in a public sector, insurance companies, banks, universities or in other organizations.
5. Land holding on irrigated land is 85% or more of the statutory ceiling area
6. Parents income is more than Rs. 4.5 Lakhs per year

## ANNEXURE- V

### **Format of Physical Disability Certificate (For Physically Handicapped Candidates)**

Certificate No. \_\_\_\_\_

Date: \_\_\_\_\_

This is to certify that I have examined Mr./Ms. \_\_\_\_\_  
Son/Daughter/Wife of Mr. \_\_\_\_\_ age \_\_\_\_\_  
Sex \_\_\_\_\_ on \_\_\_\_\_.

Photograph  
of the Candidate  
Showing the  
Physical  
Disability

- (i) He/She is suffering from \_\_\_\_\_  
(name of the physical disability),  
which comes under the sub category Blindness /Low vision/Speech & Hearing impairment/Orthopaedic disorder/Learning disabilities. Dyslexia, Dyscalcula, Dysgraphica, Spastic.
- (ii) The percentage of disability is \_\_\_\_\_ %.
- (iii) The disability is permanent in nature.
- (iv) This condition is progressive/non-progressive/likely to improve/not likely to improve. Reassessment of this case is not recommended/is recommended after a period of \_\_\_\_\_ years \_\_\_\_\_ months.
- (v) The candidate is capable of carrying out all activities related to theory and practical work as applicable to Diploma/Postgraduate Course in Electronics Engineering & IT without any special concession and exemptions.
- (vi) This certificate is issued as per the provisions given in the Persons with Disability Act, 1995 and its amendment.

This Certificate is issued for the purpose of his/her admission to first year of three (3) years Diploma in Electronics Production & Maintenance (DEPM)/Postgraduate course M. Tech. (EDT) for the Academic Year 2009-10 at DOEACC Centre, Aurangabad (MS).

Director

All India Institute of Physically  
Handicapped, Mumbai  
OR  
Dean/Civil Surgeon

Seal of Institution/Hospital

Date: \_\_\_\_\_

Place: \_\_\_\_\_

## ANNEXURE- VI (A)

### **Sponsorship Certificate (For sponsored candidates only)**

The Director  
DOEACC Centre (Formerly CEDTI),  
University Campus,  
Aurangabad 431 004

Sub: Sponsoring of an employee for M Tech (EDT) Course.

Sir,

I/We hereby sponsor the candidature of Mr. / Ms. \_\_\_\_\_  
son/daughter/wife of \_\_\_\_\_, an employee of our  
organization, for undergoing M Tech course in Electronics Design and Technology at your  
institute as Full Time candidate. He/She is working with our organization since  
\_\_\_\_\_ as \_\_\_\_\_  
(designation)

The candidate is sponsored without project.

The candidate is sponsored with a project.

The Project title is \_\_\_\_\_

We will abide to the guidelines/rules framed by DOEACC Centre (CEDTI), Aurangabad for the purpose of sponsorship of the candidate.

In case he/she is selected for the course, we shall bear the entire expenses of his/her studies. We shall relieve him/her of his/her duties for the entire period of his/her M Tech Course/ to enable him/her to devote full time to the studies

Yours faithfully

Signature

Name & Designation

Seal of Sponsoring Organization

Date:

Place:

- Note:**
1. Prepare the Sponsorship Certificate on the company letterhead as per the format given above.
  2. Please read the guidelines before preparing the sponsorship certificate.
  3. Please tick appropriate box (In case box is not properly ticked, the application *is liable to be rejected*).

## **ANNEXURE - VI (B)**

### **Guidelines for Industry Sponsorship**

Before sponsoring the candidate for the M. Tech course, the employer/sponsorer is required to carefully read the guidelines, mentioned below, for the issue of Sponsorship Certificate. Application with incomplete Sponsorship Certificate is liable to be rejected.

1. Candidate sponsored alongwith a project shall be given preference.
2. Sponsorship is meant to help industry/institution by way of sponsoring the candidate, who can pursue the project of their importance at DOEACC Centre, Aurangabad (MS)
3. Industry/Institution sponsoring the candidate with project is required to deposit the advance of Rs. 10,000/- (Rupees Ten Thousand only) per candidate, sponsored for pursuing the project. The industry also required to bear total expenditure incurred on the project. Similarly the Industry/Institute is required to reimburse all the expenditure incurred by the DOEACC Centre, Aurangabad on account of the project of their sponsored candidate, otherwise the result of the final examination alongwith the project of the sponsored candidate will be withheld.
4. It will be responsibility of the candidate to complete the project in all respect as required by his/her sponsoring industry.
5. The candidate is required to work on the project till sponsoring industry and DOEACC Centre, Aurangabad are satisfied.
6. The project is jointly evaluated by both industry and DOEACC Centre, Aurangabad.
7. Industry is required to issue a certificate / letter of regarding acceptability of the project. Based on this acceptability, the exam of the candidate is conducted.
8. Sponsoring Industry will receive the prototype developed by the candidate, if industry bears the total expenditure incurred on the project. Right of the intellectual property generated by the candidate will be the property of DOEACC Centre, Aurangabad.

**ANNEXURE- VII**

**Studentship Certificate**

**(Certificate from the Head of the Institution for Candidates in X Standard / Final Year of Qualifying Degree Course, Applying for DEPM or M. Tech [EDT])**

This is to certify that Mr./ Ms. \_\_\_\_\_  
son/daughter/wife of \_\_\_\_\_ is a regular  
student of X<sup>th</sup> standard / Final Year of \_\_\_\_\_ Course in this institution.  
He/She is appearing for the final exam in \_\_\_\_\_

(mm/yyyy)

Your faithfully

Signature

Name & Designation

Seal of Sponsoring Organization

Date:

Place:

## IMPORTANT DATES TO REMEMBER

Sr. No.	ACTIVITY	DEPM	M.TECH (EDT)
1.	Sale of application form begins	11 <sup>th</sup> June, 2009	11 <sup>th</sup> June, 2009
2.	Last date for request of application form by post	29 <sup>th</sup> June, 2009	29 <sup>th</sup> June, 2009
3.	Last date for sale of application form in person	06 <sup>th</sup> July, 2009	06 <sup>th</sup> July, 2009
4.	<b>Last date for receipt of filled in application form</b>	<b>06<sup>th</sup> July 2009</b>	<b>06<sup>th</sup> July, 2009</b>
5.	<b><u>WRITTEN TEST :-</u></b>	<b><u>25<sup>th</sup> July, 2009</u></b>	<b><u>01<sup>st</sup> &amp; 05<sup>th</sup> Aug, 2009</u></b>
6.	Interview	26 <sup>th</sup> to 27 <sup>th</sup> July, 2009	01 <sup>st</sup> to 02 <sup>nd</sup> Aug, & 06 <sup>th</sup> & 07 <sup>th</sup> Aug, 09
7.	Declaration of list of selected & waitlisted Candidates	27 <sup>th</sup> to 29 <sup>th</sup> July, 2009	02 <sup>nd</sup> to 03 <sup>rd</sup> Aug & 07 <sup>th</sup> Aug, 2009
8.	Registration	28 <sup>th</sup> & 29 <sup>th</sup> July, 2009	02 <sup>nd</sup> to 04 <sup>th</sup> Aug, 07 <sup>th</sup> Aug, 2009
9.	Academic calendar starts	29 <sup>th</sup> July, 2009	03 <sup>rd</sup> Aug, 2009